Universida_{de}Vigo

Subject Guide 2016 / 2017

*		PKXXXXX	Subjec	t Guide 2010 / 2017
IDENTIFYI				
	s and Photonics for Communications			
Subject	Electronics and			
	Photonics for Communications			
Code	V05M145V01202			
Study	Telecommunication			
	Engineering			
		Choose	Year	Quadmester
Descriptors		Mandatory		2nd
Teaching	 Spanish	Mandatory	150	
language	English			
Departmen				
	Fernández Barciela, Mónica			
Lecturers	Díaz Otero, Francisco Javier			
	Fernández Barciela, Mónica			
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General	The aim of the subject is that the student adquires knowledge	edge on the actu	al implementation	of transceivers for
	the modern communication systems that transmit in the RF and MW transceivers, the student will learn to evaluat analog circuits (active and passive) for them. As an learn simulators. In the field of the optical communications, the student wi reception components and active optoelectronical subsys them as function of the optical system to be designed. In this course the student will handle technical and scient	e performance, s ng aid, the stud l learn the opera tems , and will b	select and design c ent will use comme ation of the basic tr be able to character	omponents and rcial circuit ansmission and rise them and select
Competen	cies			
Code				
areas.	ne ability to project, calculate and design products, proces			
compa	ne capacity for mathematical modeling, calculation and sir nies, particularly in research, development and innovation ering and associated multidisciplinary fields.			
	ne ability to develop radio communication systems: antenning; link budgeting; and planning.	a, equipment ar	id subsystems desig	gn; channel
C3 CE3 TI	ne ability to implement systems by cable, line, satellite, in			
digital	The ability to use programmable logic devices, as well as to . The ability to design communications components such a nt bands.			
C13 CE13	The ability to apply advanced knowledge of photonics, opto	electronics and	high-frequency ele	ctronics.
Learning o	outcomes			
Expected re	esults from this subject			Training and Learning Results
(*)1.Conoci cada una d	miento de los distintos tipos de Proyectos y distintas fases e ellas	del Proyecto y c	omo se actúa en	
Learn to ev passive) foi	aluate preformance, select and design components and an communication transceptors in diferent frequency bands I, the student will use circuit simulators.			B1 B4 C2 C3 C12

Learn the operation of the components and basic transmission and reception active optoelectronical B1 subsystems in optical communications and photonic processing, and being able to characterise them and select them as function of the optical system to design. C2

C2 C3 <u>C13</u> C13

Handle technical documentation and scientific bibliography in English

Contents			
Торіс			
1. Introduction to circuit design for RF and	a. Analog circuits for communication transceptors.		
Microwave transceptors	b. Transceptor technologies for communication systems transmitting at		
	different frequency bands. Applications.		
	c. Basic concepts. Transmission lines. S parameters. Smith Chart.		
	Impedance matching.		
2. Passive circuit design	Couplers, filters and phase shifters.		
3. Introduction to microwave linear amplifier	 Power and power gain definitions. Gain and noise circles. 		
design	b. Stability. Stability circles. Bias and stabilization networks.		
 Microwave linear amplifier design 	a. Maximum transducer gain design		
	b. Low noise ampliifiiers		
	c. Broadband amplifiers		
5. Power amplifier design	a. Loadline and power contours.		
	b. Operating Classes.		
	c. Designing for linearity and efficiency.		
6. Frequency converters design	Frequency multipliers and mixers.		
7. Signal generators	a. Oscillator design. VCOs		
	b. PLL basics		
	c. PLL based synthesiers.		
	d. Direct digital synthesis.		
8. Photonics	a. Semiconductors optical properties.		
	b. Fabry-Perot lasers and DFB.		
	 c. Photodetectors. Static and dynamic regime. 		
	 d. Electro-optic and electro-absorbing modulators. 		

Planning			
	Class hours	Hours outside the	Total hours
		classroom	
Practice in computer rooms	8	0	8
Master Session	29	72.5	101.5
Short answer tests	1	0	1
Troubleshooting and / or exercises	2	4	6
Practical tests, real task execution and / or	0	8.5	8.5
simulated.			

*The information in the planning table is for guidance only and does not take into account the heterogeneity of the students.

	Description
Practice in computer rooms	 With the aid of a commercial microwave and RF circuit simulator, the student will analyze various passive (matching networks, filters, couplers, etc.) and active (amplifiers, oscillators) circuits. It will be defined and evaluated different figures of merit and other parameters that will be used for circuits performance evaluation. The work of the student in these practice classes will be evaluated: 1. In continuous evaluation: by answering -in writting form- short questions and performing simple designs during some/one of the practices.
	2. In evaluation performed only in a final examination: by means of short questions and circuit designs related with the work performed during the practice in computer rooms. In these practices the student with work towards achieving competencies: CE2, CE3, CE12 y CE13
Master Session	It will take place in a classroom with video projection facilities and blackboard. During these sessions it will be described in detail most of the contents in the subject programme. Competencies under work: CE2, CE3, CE12 y CE13

Personalized attention Methodologies Description

Master Session	During the master sessions the lecturer will answer the questions addressed by the students. The students will be also guided by the lecturer during the time assigned for personalized attention in his/her office, in which he/she will resolve their questions related to theoretical and practical work, as well as the design work.
Practice in computer rooms	During the practice in computer rooms the lecturer will answer the questions addressed by the students and guide his/her assigned work.

	Description C		QualificationTraining a	
			Learning Results	
Practice in computer roon	nsThe work of the student in these practice classes will be evaluated: 1. In continuous evaluation: by answering -in writting form- short questions and performing simple designs during some/one of the practices.	10	C2 C3 C12 C13	
	In evaluation performed only in a final examination: by means of short questions and circuit designs related with the work performed during the practice in computer rooms.			
	In these practices are evaluated competencies: A20, A21, A30, A31			
Short answer tests	There will be 2 short examinations, one of them in the same date as the final examination of the students that do not follow continuous evaluation. The two short examinations and the final examination will include both short answer tests and exercises. In these short examinations it will be evaluated competencies : A20, A21, A30, A31	e 30	C2 C3 C12 C13	
Troubleshooting and / or exercises	The 2 short examinations, mentioned above, and the Final Exam will include exercises resolution.	40	C2 C3 C12 C13	
Practical tests, real task execution and / or simulated.	Competencies under evaluation: A20, A21, A30, A31 For students following continous evalutation, it will be mandatory to perform a circuit desing using the circuit simulator, work proposed by the lecturer. This work will be evaluated by a written report and answers to short questions addressed by the lecturer.	20	C13 C2 C3 C12 C13	
	Competencies under evaluation :A20, A21, A30, A31			

Other comments on the Evaluation

A) If the student chooses continuous evaluation:

1. It will be compulsory the assistance to the practises in the computer room, as well as the realisation of a design of a microwave circuit by means of the circuit simulator. This design will be proposed by the lecturer and it will be an autonomous work of the student.

The evaluation of the practises will be a 10% of the total subject qualification, and the evaluation of the circuit design will be a 20%. That is to say, the sum of the evaluation of the practical classes and the design will add up to a 30% of the subject qualification.

2. The rest of the subject assessment (up to a 70% of the subject qualification) will be performed by two short exams that will contain exercises resolution, and/or short answers tests. The first short exam will assess up to a 30%, and the second up to a 40%, of the subject qualification. Before performing the second short exam, the student must inform the lecturers about his choice of the method of evaluation.

B) If the student chooses a final exam:

It will only be considered the score he/she obtained in the final examination: in the exercises resolution (in the extensive version) and in the short question test related to: the theoretical part, and the practices in the computer room. Second Assessment (July):

In July the students who did not pass the subject in May, will be assessed by an similar exam as that described in previous B option. In particular, the students that in May chose continuous evaluation and declare the want to keep the scores obtained in the practises and in the design (that will add up to a 30% of the subject qualification), will perform a reduced version of the final examination described in the previous paragraph (and will add up to a 70% of the subject qualification).

In case of plagiarism detection in any of the student works, the grade obtained by the student in this course will be a failing grade (0) and the course lecturer/s will communicate this issue to the school Board of Directors so they may take those measures deemed appropriate.

Recommendations

Subjects that continue the syllabus

Microwave and Millimetre Wave Circuit Design and CAD/V05M145V01317