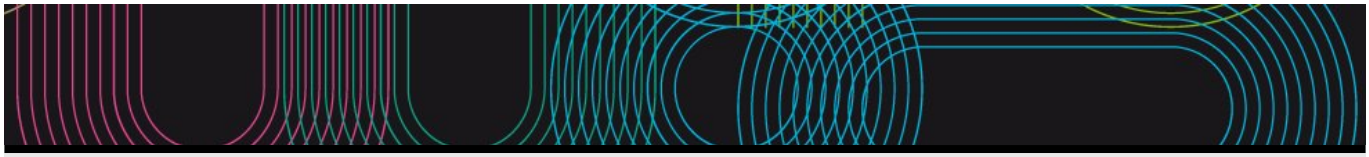

IDENTIFYING DATA				
(*)Deseño de aplicacións con microcontroladores				
Subject	(*)Deseño de aplicacións con microcontroladores			
Code	V05G300V01921			
Study programme	(*)Grao en Enxeñaría de Tecnoloxías de Telecomunicación			
Descriptors	ECTS Credits	Choose	Year	Quadmester
	6	Optional	4th	1st
Teaching language	Spanish Galician			
Department				
Coordinator	Río Vázquez, Alfredo del			
Lecturers	Costas Pérez, Lucía Río Vázquez, Alfredo del			
E-mail	ario@uvigo.es			
Web	http://webs.uvigo.es/ario/docencia/dam/dam.htm			
General description	Design and development of microcontroller-based applications, including design methodologies to develop real time applications, peripheral components configuration and connectivity.			

Competencies	
Code	
A67	(CE58/OP1) The ability to design hardware and software systems based on microcontrollers.
A68	(CE59/OP2) The ability to use software tools for microcontrollers simulation.

Learning aims	
Expected results from this subject	Training and Learning Results
Ability to know in deep the design methodologies of microcontroller-based electronic systems.	A67
Ability to configure peripheral components and to connect them to the microcontroller.	A67
Ability to know in deep the software design of the microcontroller-based electronic systems.	A67 A68
Ability to design microcontroller-based instrumentation systems and the connection between several microcontrollers.	A67 A68
Ability to know and to use design methodologies of microcontroller-based real time applications.	A67 A68

Contents	
Topic	
Introduction. Previous topics review.	Introduction. Previous topics review.
Instruction set. Addressing modes.	Instruction set. Addressing modes.
Input/Output.	Input/Output.
Timers.	Timers.
Excepctions and interrupts.	Excepctions and interrupts.
Analog interface.	Analog interface.
Compare Mode.	Compare Mode.
Power-Managed modes.	Power-Managed modes.

Planning			
	Class hours	Hours outside the classroom	Total hours
Laboratory practises	12	38	50

Master Session	12	33	45
Troubleshooting and / or exercises	5	15	20
Tutored works	7	22	29
Short answer tests	2	0	2
Short answer tests	2	0	2
Practical tests, real task execution and / or simulated.	2	0	2
*The information in the planning table is for guidance only and does not take into account the heterogeneity of the students.			

Methodologies

	Description
Laboratory practises	The students will perform simulations and electronic circuits.
Master Session	The lecturer will explain in the classroom the subject contens.
Troubleshooting and / or exercises	The lecturer will solve exercices related to the subject contebts.
Tutored works	The students have to develop a project. The lecturers will help and monitor them.

Personalized attention

Methodologies	Description
Tutored works	The lecturers will be available to help students in order to deal with the contents of the subject, the laboratory practices as well as the monitored work. The students can go to the lecturer's desk (individually or in a group). The timetable will be available on the subject website at the beginning of the term.
Laboratory practises	The lecturers will be available to help students in order to deal with the contents of the subject, the laboratory practices as well as the monitored work. The students can go to the lecturer's desk (individually or in a group). The timetable will be available on the subject website at the beginning of the term.
Master Session	The lecturers will be available to help students in order to deal with the contents of the subject, the laboratory practices as well as the monitored work. The students can go to the lecturer's desk (individually or in a group). The timetable will be available on the subject website at the beginning of the term.
Troubleshooting and / or exercises	The lecturers will be available to help students in order to deal with the contents of the subject, the laboratory practices as well as the monitored work. The students can go to the lecturer's desk (individually or in a group). The timetable will be available on the subject website at the beginning of the term.

Assessment

	Description	Qualification
Tutored works	The students will be asked to elaborate a report related to the project they have to carry out. The lecturer will also assess the student's work developed during the laboratory sessions.	20
Short answer tests	Exam to evaluate the knowledge acquired by the student after the first part of the subject. It is carried out in a classroom sesión.	25
Short answer tests	Exam to evaluate the knowledge acquired by the student related to the second part of the subject. It is carried out in a classroom session.	25
Practical tests, real task execution and / or simulated.	Laboratory exam. The student has to deal with some real and/or simulated tasks and answer several questions.	30

Other comments on the Evaluation

CONTINUOUS ASSESSMENT:

A continuous assessment learning scheme will be offered to the students:

- Two partial exams will be held related to the theory (A sessions).

- The laboratory work will be assessed by means of an exam (B sessions).

- The student has to elaborate a report describing the monitored project (C sessions).

The first partial exam will take place in the classroom after the first six sessions approximately. It will last ninety minutes. If the student passes this part, he/she is not required to retake it. In this case, after finishing the term, he/she has to take only the second partial exam. The date will be specified in the academic calendar.

In partial exams, a minimum score (5 out of 10) is required in order to get a pass.

The laboratory exam will take place at the laboratory during the last session.

In order to assess the monitored project, the lecturer will consider the quality of the final report, the work in the laboratory and the student's behavior.

The final mark (FM) is calculated as the weighted average of the three individual marks. The formula will apply a weight of 50% to the theory mark (TM), a 30% to the laboratory mark (LM) and a 20 % to the project mark (PM):

FM = 0,5*TM + 0,3*LM + 0,2*PM

The minimum passing score required in order to get a pass in the subject is 5.

When a student takes the first partial exam, it is considered that he/she choose the continuous assessment scheme and he/she will be assessed in June.

FINAL EXAM:

Students who refuse the continuous assessment scheme will be assessed by means of a final exam to evaluate the theory. The exam will be the same for them as for the students who fail the first partial exam.

The assessment of the laboratory for these students will be carried out by means of a laboratory exam. The date will be fixed within the examination period.

In this case, the final mark (FM) is calculated as the weighted average of the two individual marks. The formula will apply a weight of 50% to the theory mark (TM) and a 50% to the laboratory mark (LM):

FM = 0,5*TM + 0,5*LM

The minimum passing score required in order to get a pass in the subject is 5.

IMPORTANT REMARK:

Students who refuse the continuous assessment scheme have to contact the lecturer at least two weeks before the exam date. It is necessary to organize the laboratory exams.

FINAL EXAM (JULY):

The assessment policy in July follows the scheme described in the previous section (FINAL EXAM).

Sources of information

F. E. Valdés Pérez, R. Pallás Areni, **Microcontroladores. Fundamentos y Aplicaciones con PIC.**, Marcombo,

http://ww1.microchip.com/downloads/en/DeviceDoc/41303F.pdf, **PIC18FXXK20 Data Sheet**,

http://ww1.microchip.com/downloads/en/DeviceDoc/52116A.pdf, **PICkit 3 In-Circuit Debugger/Programmer User's Guide**,

http://ww1.microchip.com/downloads/en/DeviceDoc/41370C.pdf, **PICkit 3 Debug Express PIC18F45K20 MPLAB® C Lessons**,

Recommendations

Subjects that it is recommended to have taken before

(*)Circuítos electrónicos programables/V05G300V01502
(*)Instrumentación electrónica e sensores/V05G300V01621