# Universida<sub>de</sub>Vigo

Subject Guide 2017 / 2018

IDENTIFYIN				
	Signal Processing			
Subject	Real-Time Signal			
	Processing			
Code	V05M145V01301			
Study	Telecommunication			
	Engineering			
Descriptors	ECTS Credits	Choose	Year	Quadmester
	5	Optional	2nd	1st
Teaching	English	·		
language				
Department		'		
Coordinator	Martín Herrero, Julio			
Lecturers	Martín Herrero, Julio			
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Web	· · · · · · · · · · · · · · · · · · ·			
General description	We deal with different architectures and technique processors (DSP) and multicore computing platfor OpenCL, OpenMP, PPL and AMP will be addressed capability to adapt to new, emerging, constantly	rms (CPUs and massiv . Our main focus will b	rely parallel GPU be on hands-on,	s). Standards such as

## Competencies

Code

- B1 CG1 Ability to project, calculate and design products, processes and facilities in telecommunication engineering areas.
- B8 CG8 Ability to apply acquired knowledge and to solve problems in new or unfamiliar environments within broader and multidiscipline contexts, being able to integrate knowledge.
- C21 CE21/PS1 Manage implementation of signal processing systems options to accelerate computationally complex algorithms.

Learning outcomes		
Expected results from this subject	Training and	
	Learning Results	
To handle advanced architectures for realtime signal and video processing	B1	
	B8	
	C21	
To apply advanced techniques of DSP programming in realtime signal applications	B1	
	B8	
	C21	
To understand the basic principles of realtime signal and video processing on standard GPUs and general		
purpose GPU	B8	
	C21	
To understand and apply the fundamentals of realtime application programming on graphic processing	B1	
units, using multiplatform programming interfaces (OpenCL)	B8	
	C21	

Contents	
Topic	
High and low level DSP programming	High and low level DSP programming
GPU programming fundamentals	GPU programming fundamentals
General purpose programming of GPUs (GPGPU)	General purpose programming of GPUs (GPGPU)
OpenCL programming and integration in differen architectures	t OpenCL programming and integration in different architectures

## **Planning**

	Class hours	Hours outside the classroom	Total hours
Master Session	8	0	8
Practice in computer rooms	17	0	17
Projects	0	95	95
Long answer tests and development	2	0	2
Practical tests, real task execution and / or simulated	3	0	3

\*The information in the planning table is for guidance only and does not take into account the heterogeneity of the students.

Methodologies	
	Description
Master Session	General introductions to fundamental concepts. All competencies are addressed.
Practice in computer	Individual hands-on work on computing platforms and/or simulators to implement and compare
rooms	study cases. All competencies are addressed.
Projects	In-depth practical development of an application/algorithm according to the specific interests of
	each student. All competencies are addressed.

Personalized attention			
Methodologies	Description		
Projects	The professor will review with the student the design and the code of the student in each class session, and in individual office hours.		
Practice in computer rooms	The professor will review with the student the design and the code of the student in each session.		

Assessment				
	Description	Qualification Training and Learning		
				Results
Long answer tests and development	Questions on general fundamental concepts of	30	B1	C21
	realtime signal processing		В8	
Practical tests, real task execution and /Programming of realtime algorithms		70	B1	C21
or simulated.			В8	

#### Other comments on the Evaluation

The assessment is continuous by default, based on the work carried on by the students during the lab classes and in their personal project. This can provide up to 100% of the final mark. There is an optional written final exam at the end of the period of classes, which can be used to raise the continuous evaluation mark, or as 100% of the qualification for those students not willing to follow the continuous assessment. Those students not succeeding in the first call will have access to a second call, where the whole mark will come out from the final written exam.

## Sources of information

#### **Basic Bibliography**

Sen M. Kuo, Bob H. Lee, Wenshun Tian, **Real-Time Digital Signal Processing**, 3, Wiley, 2013

Matthew Scarpino, OpenCL in Action, 1, Manning, 2012

## **Complementary Bibliography**

Gerassimos Barlas, Multicore and GPU Programming: An Integrated Approach, 1, 2015

Khronos Group, The OpenCL specifications https://www.khronos.org/registry/cl/, 2.2, 2016

Raymond Tay, OpenCL Parallel Programming Development Cookbook, 1, Packt Publishing, 2013

## Recommendations

#### Subjects that it is recommended to have taken before

Signal Processing in Communications/V05M145V01102

### **Other comments**

For a suitable progress in the course, proficiency in C and C++ programming is required.