Universida_{de}Vigo

Subject Guide 2013 / 2014

AIIIIII					Subject	
	xía electrónica					
Subject	(*)Tecnoloxía					
Cada	electrónica V05G300V01401					
Code						
Study	(*)Grao en Enxeñaría de					
programme	Tecnoloxías de					
	Telecomunicación					
Descriptors	ECTS Credits		Choose	Year		Quadmester
Descriptors	6		Mandatory	2nd		2nd
Teaching	Spanish		nanaacory	2110		
language	opanish					
Department						
	Raña García, Herminio José					
Lecturers	Cao Paz, Ana María					
	Quintáns Graña, Camilo					
	Raña García, Herminio José					
	Río Vázquez, Alfredo del					
	Valdés Peña, María Dolores					
E-mail	hrana@uvigo.es					
Web	http://faitic.uvigo.es					
General	This course devotes to the utilisation					
description	following fields: Electronics of Powe			trical installa	itions and	to the
	conversion of photovoltaic solar en	ergy and thermal				
-	-					
Competence	ies					
Code		<u> </u>				
	9: The ability to analyze and design		sequential, synchro	nous and asy	ynchrono	us circuits and the
	of integrated circuits and microproce 11: The ability to use different energy		ially photovoltais ar			ll ac tha
	nentals of power electronics and electronics a		ially photovoltaic ar	id thermal or	ies, as we	en as the
	ility to use software tools that suppo		a in onginooring			
	ility to use software tools to search f			urcoc		
DD THE ab	inty to use software tools to search i		Dibilographical reso	urces		
Learning a						
Expected re	sults from this subject				Traini	ng and Learning
051170.0						Results
	pacity of analysis and design of com				A23	
	s and asynchronous, and utilisation o					
	apacity to use several sources of energy			italc and	A25	
	well as the fundamentals of electrote					<u> </u>
	pacity to handle software tools that				•	B4
	ity to use computer tools of researcl		resources or morm	lation.		B5
Contents						
Торіс						
1 - Amplifier	S		ots. Characteristics of			
			he bipolar junction t			
			arameters for comm			sis of one-stage
		amplitions with				
			bipolar transistor in			
		(CB) and comm	bipolar transistor in ion colector (CC)/em	nitter follower	r. Calcula	tion of the input
		(CB) and comm and output imp	bipolar transistor in ion colector (CC)/em edances, current ga	hitter follower ain and voltag	r. Calculat ge gain. S	tion of the input mall-signal model
		(CB) and comm and output imp for field-effect t	bipolar transistor in ion colector (CC)/em edances, current ga transistors (FET) in l	hitter followen ain and voltag ow frequency	r. Calculat ge gain. S /. Analysi	tion of the input mall-signal model s of one stage
		(CB) and comm and output imp for field-effect t	bipolar transistor in ion colector (CC)/em edances, current ga	hitter followen ain and voltag ow frequency	r. Calculat ge gain. S /. Analysi	tion of the input mall-signal model s of one stage

2 - Frequency response in amplifiers		els for transistors. Low-freq lifiers. Calculation of the tra	
3 - Operational amplifiers I	Ideal operational am and ideal parameter concept. Feedback e	plifier (op amp). Transfer fu s. Open-loop operation (con ffects. Closed-loop operatio plifier. Non-inverter amplifie	nparator). Feedback nal amplifier. Virtual
4 - Operational amplifiers II	adder, differential ar detector - envelope	with operational amplifiers. nplifier. Non-linear circuits: demodulator. Schmitt trigge	half wave rectifier, peak er (inverter).
5 - Electrotechnics.		lectrical installation. Protect	
6- Electronic of Power: introduction and devices	Elementary calculations of coils; calculations of Fourier: component with non-sinusoidal so distortion. 6-b: electronic powe power diode. The thy	pes of electronic power con ons in power electronics: po of rms values; apparent po nts of frequency; harmonics sources or with non-linear lo r devices. Classification. Ge yristor or SCR. The bipolar p isolated-gate bipolar transis	ower calculation; behaviour wer; factor of power; series ;; calculations of power oads; total harmonic neral characteristics. The ower transistor (BJT). The
7 DC nower cumpling		C nower supplies fories ve	ltago rogulatoro
7 - DC power supplies.	Introduction switchir	DC power supplies. Series von ng DC power supplies. DC co upplies. General diagram of	onverters: buck, boost.
8 - Rectifiers and inverters.	7-a: Rectification: Introduction. Monofasical rectifiers (half wave, double wave). Controlled and not controlled, with resistive load and with R-L load. 7-b: Single phase inverters. Topologies. Analysis of the harmonic content. PWM Inverters.		
9 - Solar energy photovoltaic and thermal conversion	Thermal and photovoltaic solar installations : The solar radiation that reaches the photovoltaic and thermal generators. Principle of operation of the photovoltaic and thermal reception installations . Thermal solar installations of high temperature. Thermal solar installations of low temperature. Isolated photovoltaic installations. Photovoltaic installations connected to power network. The solar cell. The photovoltaic generator. Design of photovoltaic systems. Generation and conversion of photovoltaic energy The battery and the voltage regulator. Types of batteries and operation modes. Types of regulators. Maximum power point tracking. Practical case of calculations of photovoltaic solar installations.		
Planning	Clean have		Total hours
	Class hours	Hours outside the classroom	Total hours
Master Session	18	18	36
Laboratory practises	22	22	44
Troubleshooting and / or exercises	6	12	18
Short answer tests	3	15	18
Troubleshooting and / or exercises	3	15	18
Practical tests, real task execution and / or simulated.	4	12	16

*The information in the planning table is for guidance only and does not take into account the heterogeneity of the students.

Methodologies	

	Description
Master Session	The teachers explain the theoretical contents.
Laboratory practises	They include circuit mounting and testing and computer electronic circuits simulation. Some practical clases will also include some web search made by the student, about some technical information about some specific electronic devices used in the practical classes (e.g. some kind of transistors or operational amplifiers).
Troubleshooting and / exercises	or The teacher will solve exercises about most of the chapters.

Personalized attention			
Methodologies	Description		

Master Session	The professor will attend personally doubts and queries of the students, on the study of theoretical concepts, on exercises or on practices of laboratory. The students will have occasion to attend to *tutorías personalised in the dispatch of the professor in the schedule that the professors will establish to such effect to principle of course and that will publish in the page of the *asignatura.
Laboratory practises	The professor will attend personally doubts and queries of the students, on the study of theoretical concepts, on exercises or on practices of laboratory. The students will have occasion to attend to *tutorías personalised in the dispatch of the professor in the schedule that the professors will establish to such effect to principle of course and that will publish in the page of the *asignatura.
Troubleshooting and / or exercises	The professor will attend personally doubts and queries of the students, on the study of theoretical concepts, on exercises or on practices of laboratory. The students will have occasion to attend to *tutorías personalised in the dispatch of the professor in the schedule that the professors will establish to such effect to principle of course and that will publish in the page of the *asignatura.

Assessment		
	Description	Qualification
Short answer tests	They make part of each partial examination of theory, in which they are half of its value. The number of tests and how they work are detailed in "Other comments and second call".	35
Troubleshooting and / or exercises	They make part of each partial examination of theory, in which they are half of its value. The number of tests and how they work are detailed in "Other comments and second call".	35
Practical tests, real task execution and / or simulated.	They are made in the laboratory. They consist of the kind of tasks made or prepared during the practices of the course: the practical exams consist of: 1) mounting of circuits, taking measures on them and answering questions related with these circuits and 2) simulation circuits equal or similar to the ones studied in the practices and answering questions related with this simulation. In the examinations of practices of laboratory the student will be allowed to use some especific technical information collected by the student during the practices (eg datasheets from manufacturers).	

Other comments on the Evaluation

1. Continuous assessment:

The student is graded by means of a continuous assessment, which consists of []partial exams[], including both written exams ([]exámenes teóricos[]) and laboratory exams ([]exámenes prácticos[]).

Nevertheless, a student may choose between that continuous assessment or a one-session exam ([examen final]). The rules of both kind of assessment are as follows:

1.1.Written exams ([exámenes teóricos]):

The theoretical contents of the course are divided into three blocks. The 1st block and the 2nd block are graded by means of a $\exists partial \text{ theoretical exam} \\$ for each (examen parcial teórico). They take place in the usual weekly scheduling of the

theoretical classes of the course. The 3rd block is graded by means of another exam which takes place only in the [final

theoretical exam[], the [](April-)May exam[]. All the student must attend at this 3rd block exam. The final theoretical exam

consists of a [block exam] for each of the three blocks of the contents. The 1^{st} and the 2^{nd} block are made only by the students who did not pass the respective partial exam (grade

Sources of information

Hambley, A. R., Electrónica, Prentice-Hall, 2ª ed. en español,

Hart, D. W., Electrónica de potencia, Prentice-Hall,

Rashid, Muhammad H., Electrónica de potencia: circuitos, dispositivos y aplicaciones, Pearson Education,

Reglamento Electrotécnico para Baja Tensión (REBT) e Instrucciones Técnicas Complementarias (ITC),

Schneider Electric España, S.A., Manual electrotécnico: Telesquemario (http://www.schneiderelectric.es), Schneider Electric España, S.A,

AENOR, Norma UNE 60617 de Símbolos gráficos para esquemas eléctricos,

Carta, J. A. y otros, "Centrales de energías renovables: Generación eléctrica con energías renovables", Pearson-UNED,

Quintáns Graña, C., Simulación de circuitos con OrCAD 16 DEMO, Marcombo,

Subjects that continue the syllabus

(*)Electrónica analóxica/V05G300V01624 (*)Electrónica de potencia/V05G300V01625

Subjects that it is recommended to have taken before

(*)Física: Fundamentos de electrónica/V05G300V01305

Other comments

En la Tecnología "Sistemas electrónicos", la asignatura "Electrónica analógica" de 3er curso, continúa una parte del temario (amplificadores operacionales).